

Notice of References Cited	Application/Control No. 09/986,650		Applicant(s)/Patent Under Reexamination WARD, DEREK	
	Examiner Ryan A. Jarrett		Art Unit 2125	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,091,263	07-2000	New et al.	326/40
	B	US-			
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	D	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Virtex-E 1.8 V FPGAs: Preliminary Product Specification". Xilinx (9/20/00) DS022 v1.7.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.